



PATENT
Docket No.: MAN-013

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: Not Yet Available

Examiner: Not Yet Available

Serial No.: 10/723,285

Filed: November 25, 2003

In re Application of: Werner, et al.

For: A SEMICONDUCTOR STRUCTURE FOR USE IN THE NEAR INFRARED
REGION AND A METHOD OF MANUFACTURING THIS SEMICONDUCTOR
STRUCTURE

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 4/2/04, Signed Carol Stanley
Carol Stanley

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed please find the following:

1. Information Disclosure Statement;
2. Form PTO-1449;
3. 15 References.

Please contact Applicant's attorney at the address, telephone or facsimile number if there are any matters whose resolution can be expedited thereby.

Respectfully submitted,
SIERRA PATENT GROUP, LTD.

Dated: April 2 2004

Sierra Patent Group, Ltd.
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Andrew D. Gathy
Andrew D. Gathy
Reg. No.: 46,441



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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Each item of information listed in the attached FORM PTO-1449, for which a copy of each is attached, may be material to the examination of the above-identified application and is, therefore, submitted in compliance with the duty of disclosure defined in 37 CFR §§1.56, 1.97 and 1.98. The Examiner is requested to make these items of official record in this application.

This Information Disclosure Statement under 37 CFR §§1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that any one or more of these items constitutes prior art.

I

This statement is filed pursuant to:

(X) 37 C.F.R. §1.97(b).

This information disclosure statement is filed either (1) within three months of the filing date of the national applications; (2) within three months of the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in an international application; or (3) before the mailing date of a first office action on the merits, whichever event occurs last.

Accordingly, this information disclosure statement requires no fee and no certification.

() 37 C.F.R. §1.97(c).

This information disclosure statement is filed after the period specified in 37 C.F.R. §1.97(b), but before the mailing date of either (1) a final action under 37 C.F.R. §1.113 or (2) a notice of allowance under 37 C.F.R. §1.311.

Accordingly, this information disclosure statement requires either the fee specified in 37 C.F.R. § 1.17 (p) or a certification according to 37 C.F.R. §1.97(e).

() 37 C.F.R. §1.97(d).

This information disclosure statement is filed after the period specified in 37 C.F.R. §1.97(c).

Accordingly, this information disclosure statement requires the fee specified in 37 C.F.R. §1.17(p), \$180.00, for submission of an information disclosure statement under 37 C.F.R. §1.97(d), and a statement according to 37 C.F.R. §1.97(e).

37 C.F.R. §1.97(e).

(X) (1) Each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement.

() (2) No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. §1.56(c), more than three months prior to the filing of the statement.

If this statement crosses in the mail with an office action, or is otherwise not in the indicated category of 37 C.F.R. §1.97, it is respectfully requested that this statement be treated in the next appropriate category and made of record. To the extent required, please treat this paper as a conditional petition for acceptance of the information disclosure statement.


II

- (X) No fee is due.
- () The fee specified in 37 C.F.R. §1.17(p) for submission of an information disclosure statement under 37 C.F.R. §1.97(c), 37 C.F.R. § 1.97(d), or 37 C.F.R. §1.97(e) is enclosed, \$180.00.

In the event any fee is required for filing the above-noted document, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing attached document timely, the Assistant Commissioner is hereby authorized to charge the fee to our Deposit Account No. 50-0612. A duplicate of this page is enclosed.

Respectfully submitted,
SIERRA PATENT GROUP, LTD.

Dated: *Apr 2 2007*


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Form PTO 1449 (Rev. 2-32)				U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No. MAN-013		Serial No. 10/723,285	
Information Disclosure Statement by Applicant						Applicant: Werner, et al.			
(Use several sheets if necessary)						Filed: 11/25/03		Group: Not Yet Avail.	
U.S. Patent Documents									
Init.		Document No.	Date	Name	Class	Subclass	Filing Date		
	1	4,959,694	09/25/90	Gell	357	16	12/23/88		
	2	5,917,195	06/29/99	Brown	257	22	02/17/95		
	3	6,403,975 B1	06/11/02	Brunner, et al.	257	15	04/08/97		
Foreign Documents									
Translation									
Init.		Document No.	Date	Country	Class	Subclass	Yes	No	
	4	EP 1 178 522 A1	02/06/02	EP	H01L 21	20		X	
	5	European Search Report	07/14/03	EP			X		
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)									
	6	Schmidt, et al., RESONANT TUNNELING DIODES MADE UP OF STACKED SELF-ASSEMBLED GE/SI ISLANDS, Applied Physics Letters, American Institute of Physics, Vol. 77, No. 26, pgs. 4341-4343, 12/25/00.							
	7	Liu, et al., OBSERVATION OF INTER-SUB-LEVEL TRANSITIONS IN MODULATION-DOPED GE QUANTUM DOTS, Applied Physics Letters, American Institute of Physics, Vol. 75, no. 12, pgs. 1745-1747, 09/20/99							
	8	Deutschmann, et al., MINIBAND TRANSPORT IN VERTICAL SUPERLATTICE FIELD-EFFECT TRANSISTORS, Applied Physics Letters, American Institute of Physics, Vol. 79, No. 10, pgs. 1564-1566, 09/03/01.							
	9	Brunner, et al., GE QUANTUM DOTS IN SI: SELF-ASSEMBLY, STACKING AND LEVEL SPECTROSCOPY, Physica E 13, pgs. 1018-1021, 2002.							
	10	Goryll, et al., MORPHOLOGY AND PHOTOLUMINESCENCE OF GE ISLANDS GROWN ON SI (001), Thin Solid films 336, Elsevier Science S.A., pgs. 244-247, 1998.							
	11	Schmidt, et al., EFFECT OF OVERGROWTH TEMPERATURE ON THE PHOTOLUMINESCENCE OF GE/SI ISLANDS, Applied Physics Letters, American Institute of Physics, Vol. 77, No. 16, pgs. 2509-2511, 10/16/00.							
	12	Eberl, et al., SELF-ASSEMBLING SIGE AND SIGEC NANOSTRUCTURES FOR LIGHT EMITTERS AND TUNNELING DIODES, Thin Solid Films 369, Elsevier Science S.A., pgs. 33-38, 2000.							
	13	Scamarcio, et al., TUNABLE INTERMINIBAND INFRARED EMISSION IN SUPERLATTICE ELECTRON TRANSPORT, Applied Physics Letters, American Institute of Physics, pgs. 1796-1798, 04/07/97.							
	14	O.G. Schmidt, et al., MULTIPLE LAYERS OF SELF-ASSEMBLED GE/SI ISLANDS: PHOTOLUMINESCENCE, STRAIN FIELDS, MATERIAL INTERDIFFUSION, AND ISLAND FORMATION, Physical Review B, The American Physical Society, Vol. 61, No. 20, pgs. 13721-13729, 05/15/00.							
	15	Manfred Helm, Topical Review, INFRARED SPECTROSCOPY AND TRANSPORT OF ELECTRONS IN SEMICONDUCTOR SUPERLATTICES, Semiconductor Science Technology 10, pgs. 557-575, 1995.							
Examiner					Date Considered				
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.									